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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):	Eliyahou Harari, Robert D. Norman and Sanjay Mehrotra		
Assignee:	SanDisk Corporation		
Title:	Flash EEprom System		
Serial No.:	09/657,482	Filing Date:	September 8, 2000
Docket No.:	11587 M-10187-39C US	Group Art Unit:	2184

San Francisco, California
September 19, 2001

COMMISSIONER FOR PATENTS
Washington, D. C. 20231

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PRELIMINARY AMENDMENT

Dear Sir:

Please amend the above-identified patent application as follows:

IN THE SPECIFICATION:

Page 1 of the specification, between lines 3 and 4, rewrite the paragraph that was added by paragraph 4 of the Continuation Application Transmittal, filed with the present continuation application, to read as follows:

Cross-Reference to Related Applications

This is a continuation of Application Serial No. 08/789,421, filed January 29, 1997, now Patent No. 6,149,316; which in turn is a continuation of Serial No. 08/174,768, filed December 29, 1993, now Patent No. 5,602,987; which in turn is a continuation of Serial No. 07/963,838; filed October 20, 1992, now Patent No. 5,297,148; which in turn is a divisional of Serial No. 07/337,566; filed April 13, 1989, now abandoned.

09/26/2001 MWOLDER1 00000079 192386 09657482
01 FC:102 240.00 CH
02 FC:103 558.00 CH

IN THE CLAIMS:

Cancel claims 56-62, all of the claims now in the present application, and substitute therefore the following new claims:

--63. A method of operating a memory that includes an array of non-volatile EEprom memory cells partitioned into a plurality of blocks that individually include a distinct group of memory cells that are erasable together as a unit, comprising:

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